



ORGANIZING COMMITTEE

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Dr. N V S N Sarma,
Director

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Dr. G. Seetharaman,
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Coordinator(s)

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Assistant Professor/ECE

Dr. R. Krishnamurthy,

Assistant Professor/ECE

Dr. C. Velmurugan,

Assistant Professor/Mechanical

Dr. N. Renugadevi,

Assistant Professor/CSE

IMPORTANT DATES

Last date for receipt of
Registration form: 06-07-2023

Intimation of selection:

07-07-2023

Mode of intimation through
email only

CERTIFICATE

The certificates shall be issued to the participants who have attended the program with minimum 80% attendance. The participants also have to provide compulsory Feed-back on the last day of FDP.

Contact:

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Science and Engineering Research Board (SERB) Sponsored High-End Workshop (Kaaryashala) under "ABHYAAS"

on

"Machine Learning based high performance & low power VLSI System Design and Testing using FPGA"

10-07-2023 to 16-07-2023

Organized by

Department of ECE, Indian Institute of Information Technology Tiruchirappalli

Highlights

- No registration fee will be charged from the participants.
- The workshop is designed only for the PG, PhD Students & Innovators.
- Food and Accommodation will be provided during the workshop to all the participants.
- TA can be claimed by producing the tickets with shortest route as per the norms.
- Online registration of participants to be done through the link given below:

<https://forms.gle/9yqCmSRmKdrkv7kd9>

About the Workshop

The evolution of Machine Learning and optimization has led to the advancements in IC Design and Fabrication Technology. This workshop explores the typical high level design, development and testing of an advanced VLSI systems from scratch. The course content includes Introduction to Advanced VLSI system design, Architecture, Algorithms and System Techniques, Hardware and Software Co-design, Hardware Architectures for ML, Reconfigurable Architectures, IPs and System-Chips, System on Chip design, Static Timing Analysis, Physical Design and Verification, Reliability, fault tolerance system design, Machine Learning (ML) and Deep Learning (DL) Models, and IoT based system design, ML based high performance computing & system design (Power, Performance and Cost) optimization. This course will provide better ideas on concepts and specifications to describe the functional behavior of the system design, ability to design and implement VLSI circuits under realistic constraints and conditions, ability to debug, verify, simulate, synthesize digital circuits, develop & implement complex digital system design using ASIC and FPGA technologies. The program also motivates the research ideas with practical applications by experts in the field of research.

About Institute

The Indian Institute of Information Technology Tiruchirappalli (IIITT) was established in the year 2013-14 as the Institute with National Importance under Public Private Partnership Mode by Ministry of Human Resource Development (MHRD), Govt. of India. The Stakeholders of IIITT are Central Govt. of India, State Govt. of Tamil Nadu, and Industry partners, viz., TCS, CTS, Infosys, Ramco Systems, ELCOT, and Navitas. The college has been conferred with autonomy in financial and administrative matters to achieve rapid development. IIIT Tiruchirappalli is operating in the permanent campus at Sethurappatti, Trichy-Madurai Highway, Tiruchirappalli, Tamil Nadu - 620012 from June 2021.